



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2836  
Examiner: Mr. Ronald W. Leja

In re PATENT APPLICATION of:

Applicants : Hiroshi TOBISAKA et al.

Serial No. : 10/800,013

Filed : March 15, 2004

For : SEMICONDUCTOR DEVICE HAVING  
TRANSISTOR WITH HIGH ELECTRO STATIC  
DISCHARGE CAPABILITY AND HIGH NOISE  
CAPABILITY

Attorney Ref: FEC 121

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

PETITION  
FOR  
EXTENSION OF TIME

November 28, 2007

Sir:

Please extend the period for responding to the Office Action of June 28, 2007 by two months, and thus until December 28, 2007.

A remittance that includes the \$460 extension fee is being submitted concurrently. Should this remittance be accidentally missing or insufficient, though, any fees that may be needed can be charged to our Deposit Account number 18-0002.

Respectfully submitted,

Allen Wood 11/29/2007 JRD/01 03000026 10000013

Registration No. 28,134

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460.00

AW/ng